

Docket No. 218207US-2 PCT

IN RE APPLICATION OF: Simon DELEONIBUS

SERIAL NO: 10/030,175

FILED: JANUARY 31, 2002

FOR: METHOD FOR MAKING AN ELECTRONIC COMPONENT WITH SELF-ALIGNED DRAIN AND GATE, IN DAMASCENE ARCHITECTURE



ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

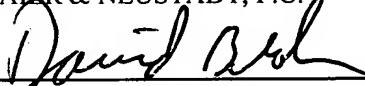
No additional fee is required  
 Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.  
 Additional documents filed herewith: Marked-up Copy of Amendment, IDS/Related/List of Related Cases, PTO Form 1449, Cited Reference (1), Related Pending Application (1)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	11	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	1	MINUS	3	0	× \$84 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS				+ \$280 =		\$0.00
						\$0.00
<input type="checkbox"/> Reduction by 50% for filing by Small Entity						\$0.00
<input type="checkbox"/> Recordation of Assignment				+ \$40 =		\$0.00
						\$0.00
				TOTAL		\$0.00

A check in the amount of \$180.00 is attached.  
 Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.  
 If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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IN RE APPLICATION OF  
SIMON DELEONIBUS

SERIAL NO: 10/030,175

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FOR: METHOD FOR MAKING AN  
ELECTRONIC COMPONENT WITH  
SELF-ALIGNED DRAIN AND  
GATE, IN DAMASCENE  
ARCHITECTURE

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

EXAMINER: PERALTA, G.

GROUP ART UNIT: 2814

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action mailed November 20, 2002, please amend this application as follows:

IN THE CLAIMS

Please amend Claims 1-10 to read as follows:<sup>1</sup>

--1. (Amended) A method for fabricating an electronic component with a self-aligned source, drain and gate, comprising the steps of:

a) forming a dummy gate on a silicon substrate, said dummy gate defining a position

for a channel of the component;

<sup>1</sup>A marked-up copy of the changes made in this amendment are attached.

TECHNOLIS, INC. 2000  
FEB 25 2003

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#7/Amend A  
3:13:03  
CMoor